LT8911EXB --- Product Brief

MIPI® DSI/CSI Bridge to eDP

Features

- **Single-Port MIPI® DSI Receiver**
  - Compliant with D-PHY1.2, DSI1.3 and CSI1.3
  - 1 clock lane and 1~4 configurable data lanes
  - 80Mb/s~2.0Gb/s per data lane
  - Data lane and polarity swapping
  - Data lane input de-skew
  - Internal Rterm calibration w/i less than 5% error
  - 3-bit programmable equalization
  - Support Burst and Non-Burst Mode
  - Support 16/18/24/30/36-bit RGB and YUV format
- **eDP1.4 Transmitter**
  - Compliant to VESA eDP1.4 standard
  - Support1/2/4 data lanes with 1.62Gbps(RBR) or 2.7Gbps(HBR).
  - Data lane and polarity swapping
  - Optional SSC 0.5% down-spreading output
  - Configurable and power-on-calibrated output swing for optimized EMI
  - Support PWM Backlight control
  - MCCS over AUX channel
- **Miscellaneous**
  - Single 1.8V supply power
  - Temperature range: -40°Cto +85°C
  - Packaged in 6mm x 6mm QFN48

Description

The Lontium LT8911EXB MIPI®DSI/CSI to eDP converter features a single-port MIPI receiver with 1 clock lane and 4 data lanes operating at maximum 2.0Gbps per data lane; a maximum input bandwidth of 8.0Gbps. The converter decodes the input MIPI® DSI 16/18/24/30/36-bit RGB packets and converts the formatted video data stream to a single-link VESA eDP1.4 compliant output with 1/2/4configurable data lanes, supporting RBR(1.62Gbps) and HBR(2.7Gbps) link speeds. The build-in optional SSC function reduces EMI effect on EMI-concerned system application.

Applications

- Mobile systems
- Digital still cameras
- Cellular Handsets
- Personal Media players
- Digital video cameras
- Gaming

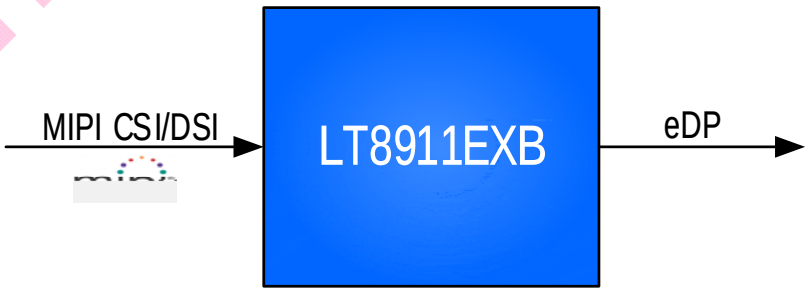

Figure 1. Application Diagram

Ordering Information

Part Number	Operating Temperature Range	Package	Packing Method
LT8911EXB	−40 °C to +85 °C	QFN48 (6*6)	Tray